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PATENT

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Applicant: Rajasekhar Pullela
 Serial No.: 10/033,525
 Filed: December 28, 2001
 Title: TRANS-ADMITTANCE TRANS-IMPEDANCE
 LOGIC FOR INTEGRATED CIRCUITS
 Grp./A.U.: 2816
 Examiner: Dinh Thanh Le

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

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